## Application form Registration for I MTECH I SEM (NECR MTECH 20) (For 2020 Regular admitted batches only) (Read instructions carefully before filling the application)

VLSI

Whether the	candida	ate is			4.															
Appearing for	or																			
	Regular Examination Supplementary Examination														1 A 66					
Month & Year of Examination														Affix a recent Passport size Photograph.						
H.T.No																				
Name: (As																				
Per S.S.C Certificate)																				
Certificate)																			]	
Father's																				
Name:																				
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Mother's																				
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Date of Birth (As per SSC Marks Memo) :																				
Student Aadh																				
Mobile Numb																				
E-Mail Id :																				
Tick $[]$ the appropriate box																				
Sex: Male Female Caste OC/Be										C/BC	C-A/B	/C/D/E/SC/ST								
Subject for which registration is required:																				
	20VL0201 Analog CMOS IC design																			
		Digit	al C	MOS																
	20VL0401 VLSI SIGNAL PROCESSING																			
20VL0406 LOW POWER VLSI DESIGN																				
	20VL5201 RTL Simulation and Synthesis with PLDs Lab																			
20VL5202 CMOS IC Desi											esign Lab									
	)101	Research Methodology and IPR																		
20AC0701							t Co	urse												
208C0501							e ado	ded o												
Certified that the above information is CORRECT and Filled by me.																				
Signature of the Candidate																				

Enclosures to the application: **Photo Copy of Result Sheet, fee Payment Receipt**